

31876.0140



PATENT
09/405,618

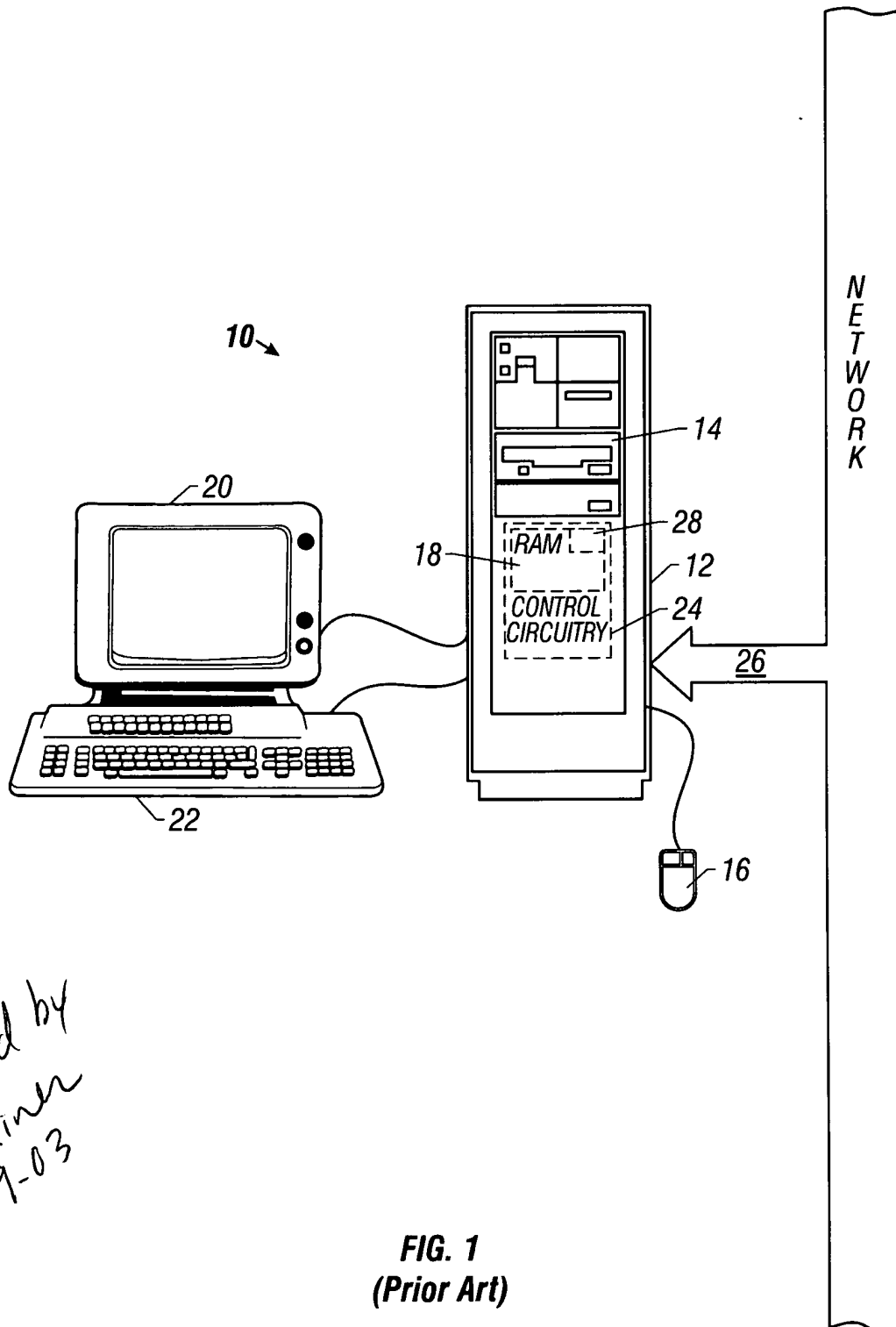
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New Formal Drawings



Title: Software Modeling of Logic Signals Capable
of Holding More Than Two Values
Inventors: Blomgren et al.
Application Serial No.: 09/405,618
Docket No.: 31876.0140

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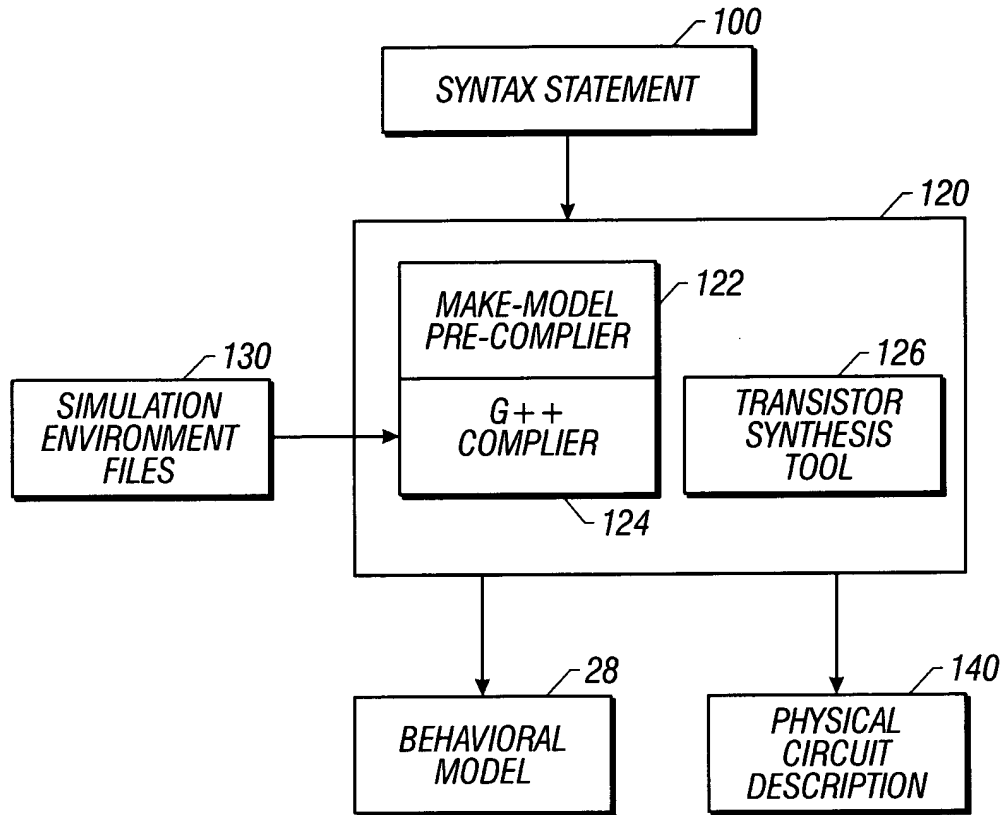


FIG. 2

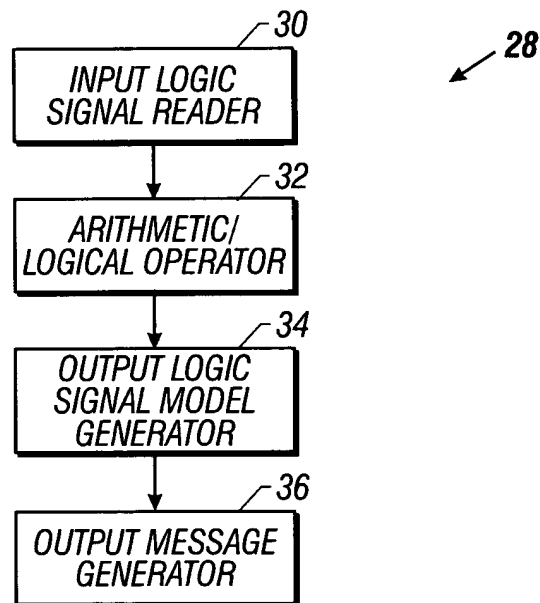


FIG. 3

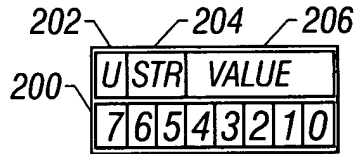
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STR	CORRESPONDS TO
11	Z - high impedance
10	R - weakly-driven
01	moderately-driven
00	strongly-driven



VALUE	CORRESPONDS TO	VALUE	CORRESPONDS TO
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01111	N-nary signal value = 15	11111	N-nary signal value = 31

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FIG. 4

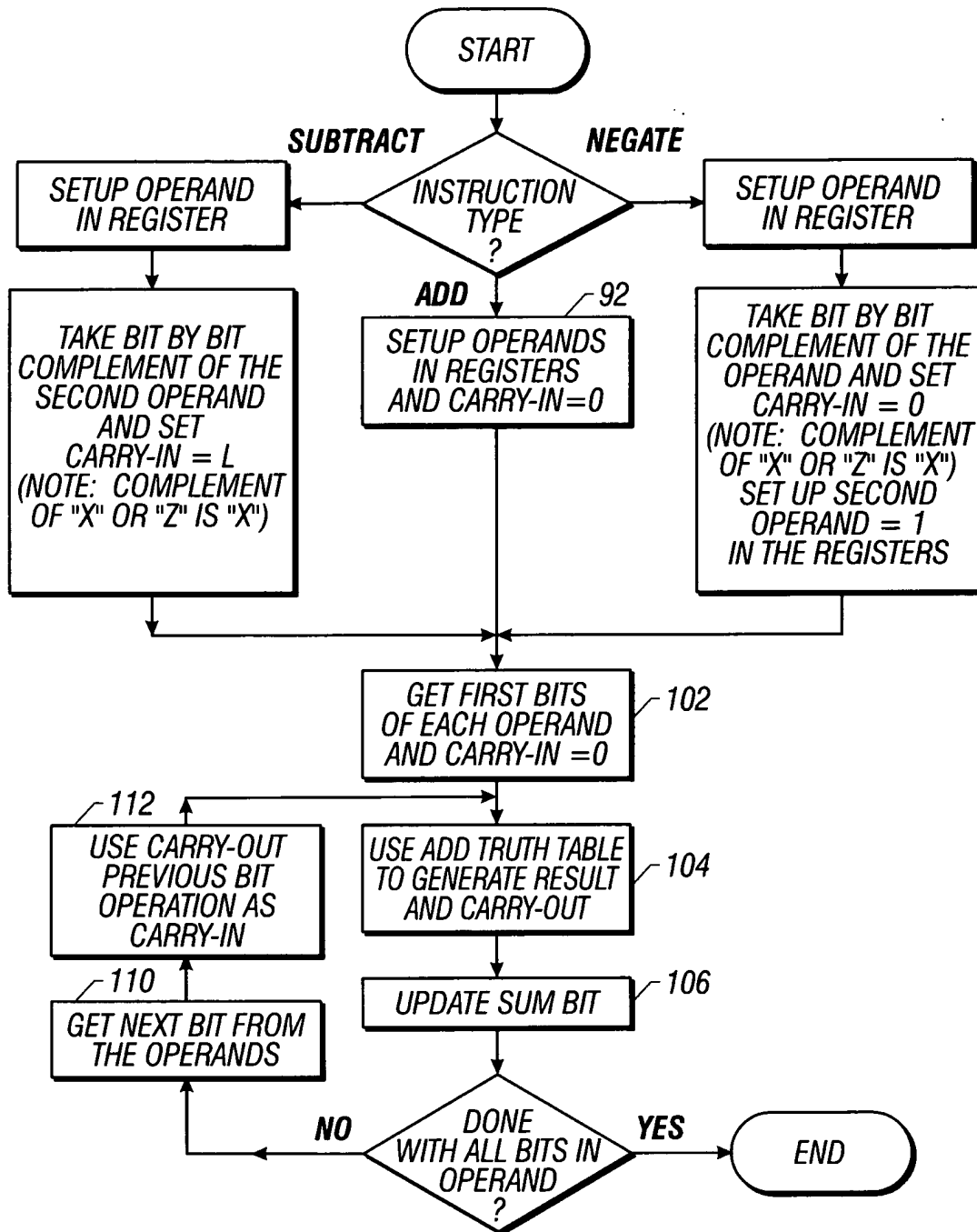


FIG. 5A

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FIG. 5B

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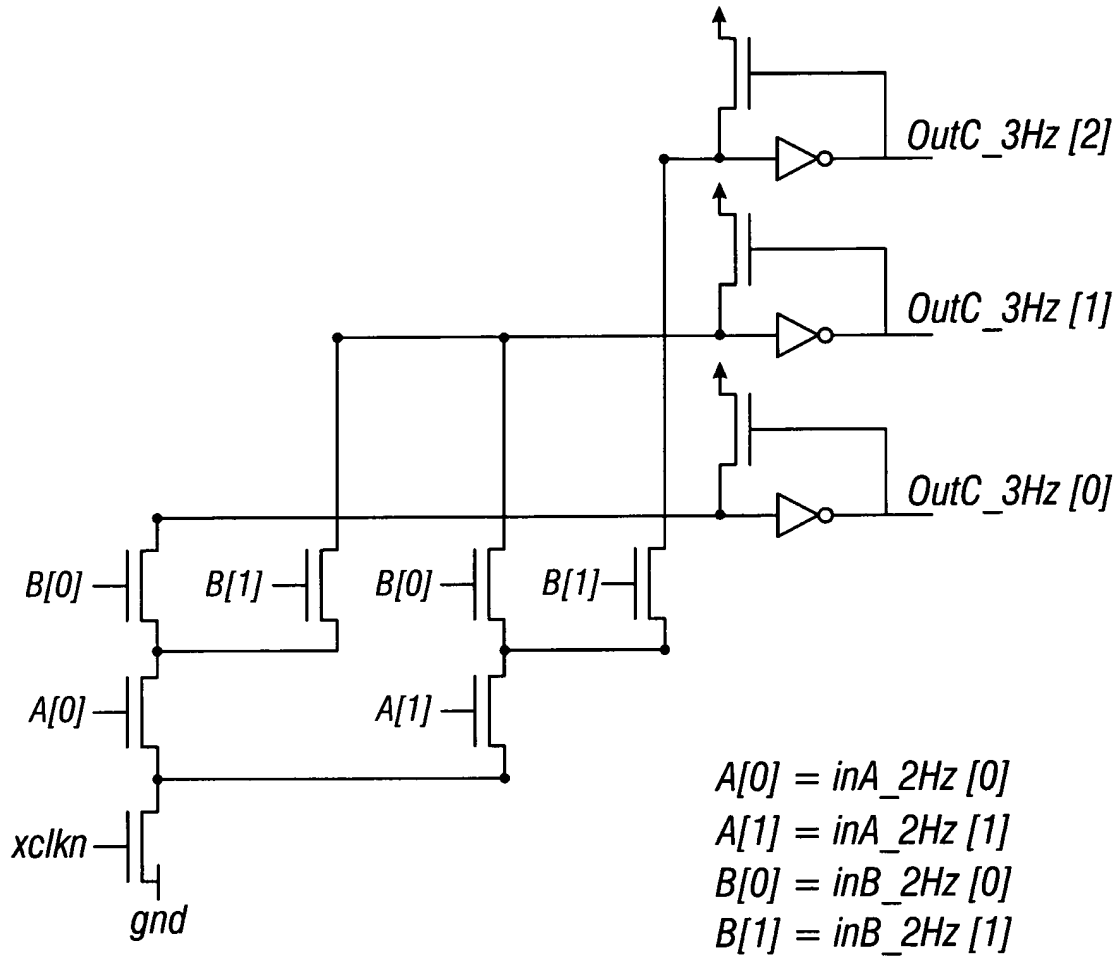


FIG. 6

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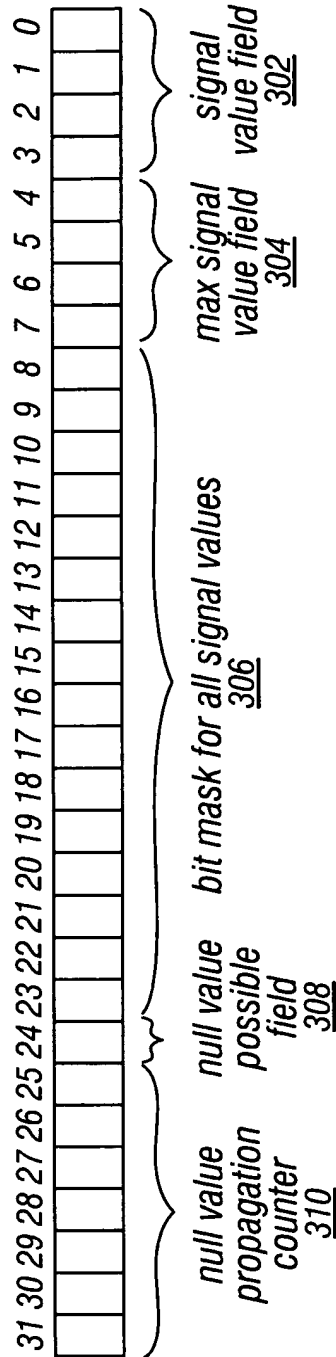


FIG. 7

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G. FIG.	
CLASS	SUBCLASS

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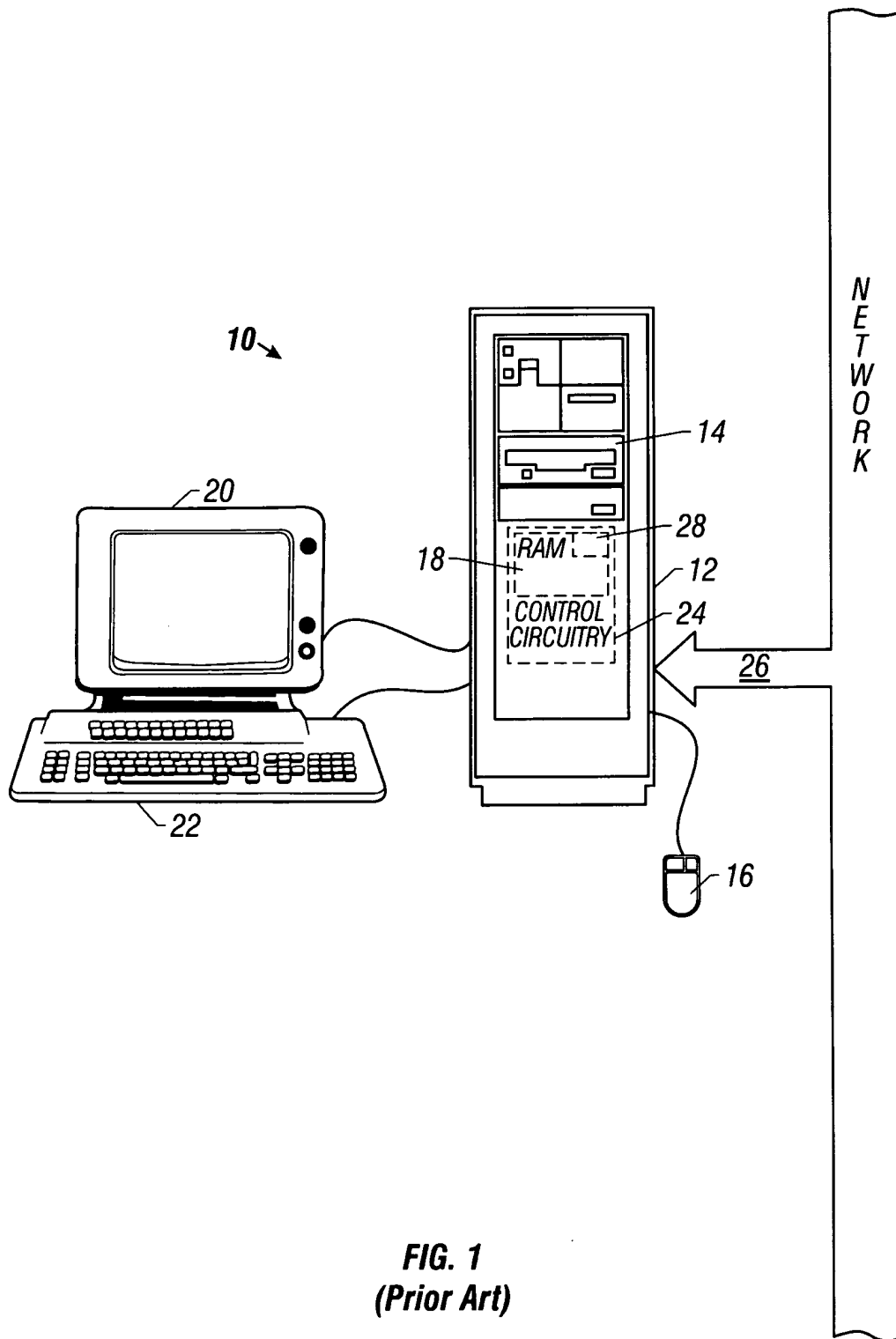
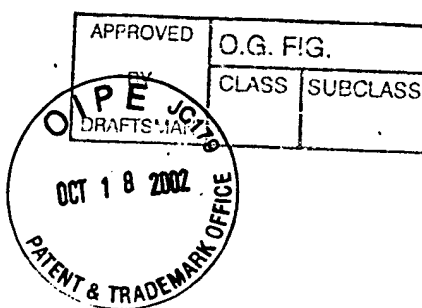


FIG. 1
(Prior Art)



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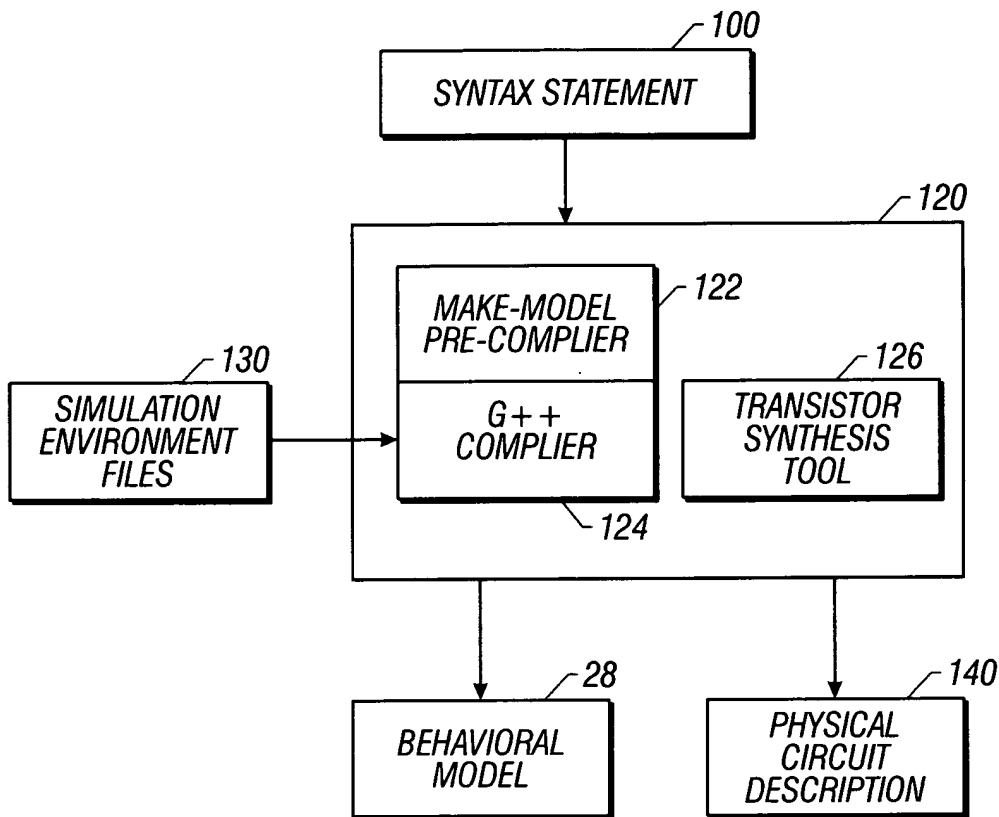


FIG. 2

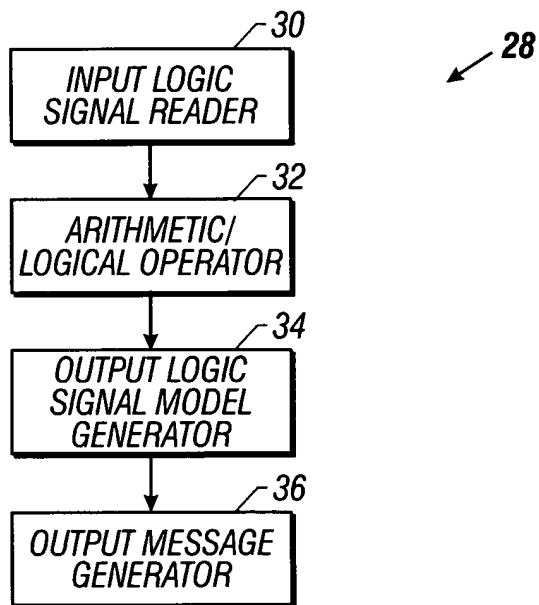


FIG. 3

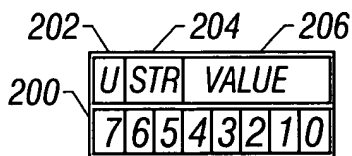


O.G. FIG.	
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FIG. 4

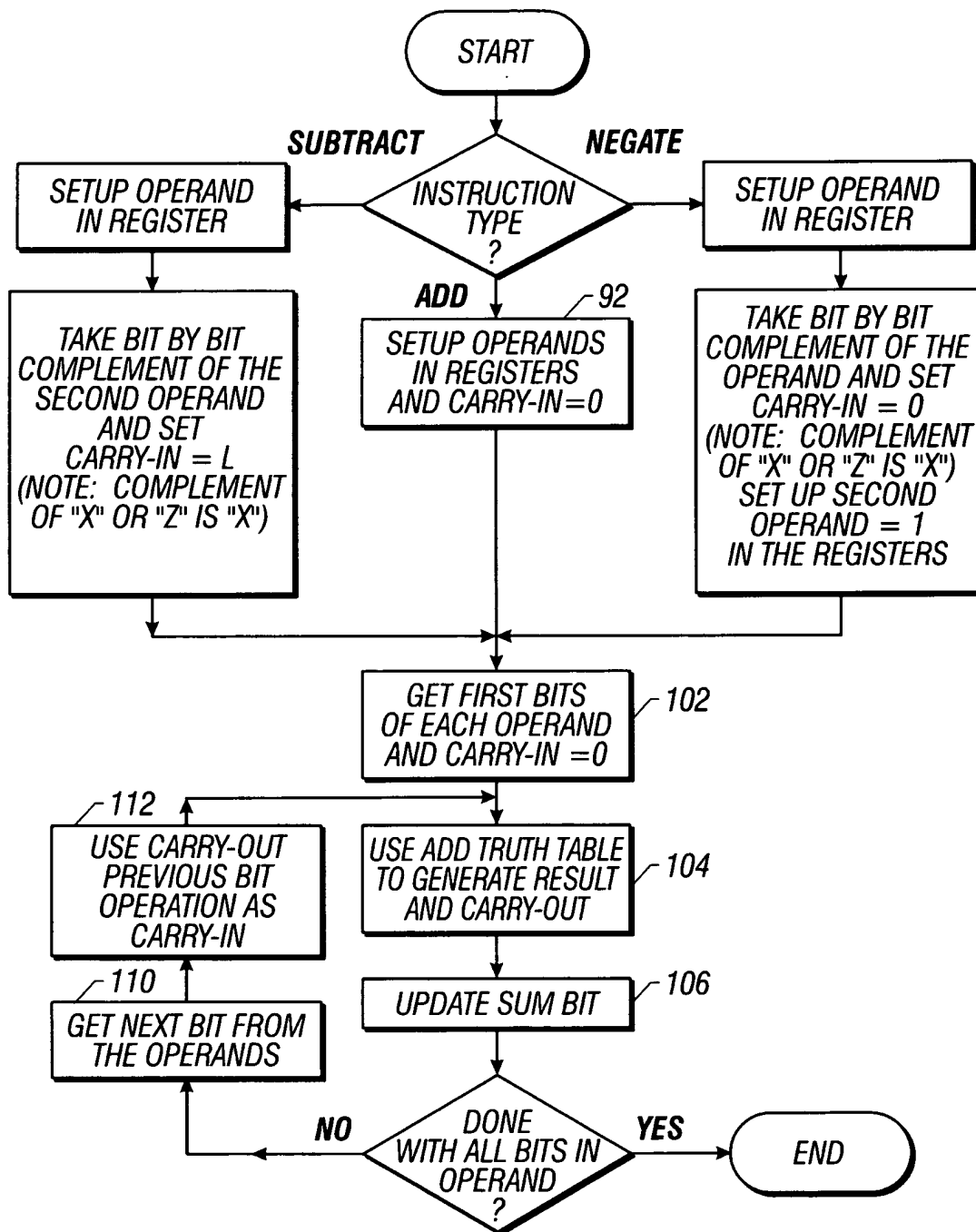
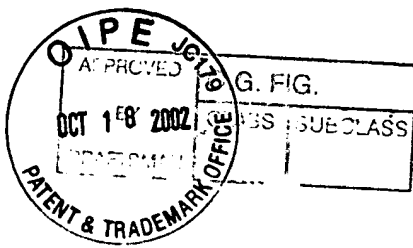


FIG. 5A

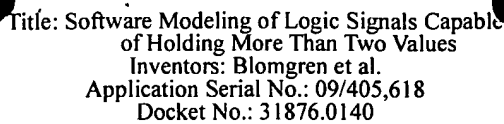
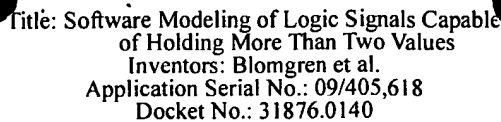
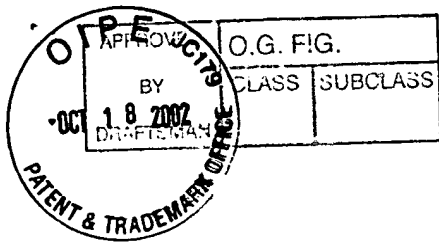


FIG. 5B



$A[0] = inA_2Hz [0]$
 $A[1] = inA_2Hz [1]$
 $B[0] = inB_2Hz [0]$
 $B[1] = inB_2Hz [1]$

FIG. 6



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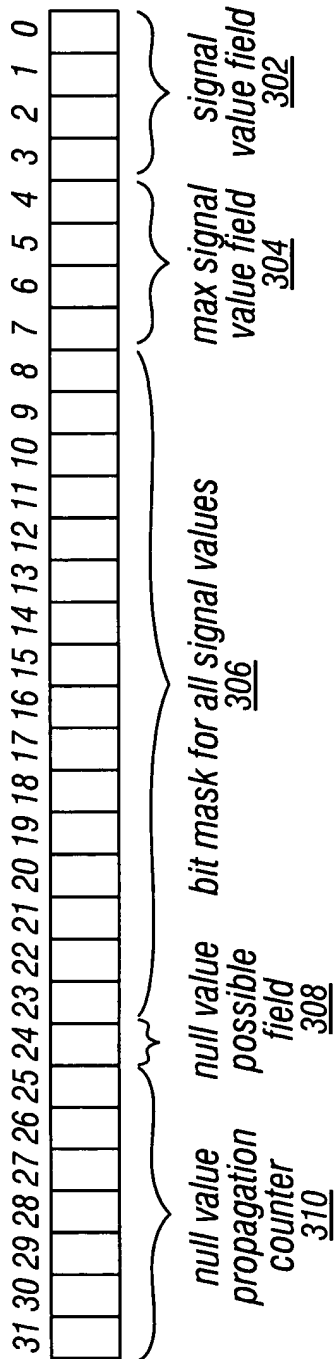


FIG. 7